mercury systems_™

EnsembleSeries™ FCN8604

Rugged AdvancedTCA dual Xeon (Haswell) processing blade with FPGA acceleration and mezzanine

- Dual Intel® 1.8GHz 12-core Xeon® EP server-class processors
- Up to 128 GB DDR4-2133 SDRAM
- Dual Intel Arria® 10 FPGA co-processors with 32 GB of DDR4 DRAM
- Two front-panel 40 Gb/s QSFP+ ports for sensor I/O streams
- Two 40 Gb/s Ethernet or InfiniBand fabric interfaces



The rugged EnsembleSeries™ FCN8604 blade combines the general purpose processing capabilities of the EnsembleSeries HDS8613 with FPGA co-processing from dual Intel Arria 10 devices. By combining I/O, FPGA co-processing and general-purpose Intel processing in a single AdvancedTCA® blade, Mercury delivers a uniquely capable architecture for sensor processing in the open standard ATCA architecture.

Dual Intel® Xeon® E5-2648L v3 x86 processors, each with 12 cores, Haswell microarchitecture and the Wellsburg Bridge (collectively called Grantley) and with the support of unrestricted 40Gb/s Ethernet and FDR10 fabric bandwidth, deliver scalable and unmatched general processing power. The two Intel Arria 10 FPGAs deliver balanced and optimized FPGA co-processing capabilities. Two QSFP+ fiber optic interfaces support front panel streaming I/O into the server-class processing blade. The optimized mechanical cooling technology ensures the highest module and system MTBF even under full throttle, continuous processing conditions.

EnsembleSeries FCN8604 blades are part of Mercury's AdvancedTCA ecosystem of building blocks that includes 40 Gb/s Ethernet/ InfiniBand™ switches and server-class general-purpose processing modules. These are PICMG 3.0 compliant modules may be quickly configured and developed in the lab using concurrent engineering disciplines and deployed in industry-standard 2, 5, or 14 slot development chassis.

Mercury Systems is a leading commercial provider of secure sensor and safety-critical processing subsystems. Optimized for customer and mission success, Mercury's solutions power a wide variety of critical defense and intelligence programs. Supporting Mercury's Xeon server-class ecosystem are Mellanox's Connect-X3 40Gb/s VPI interfaces, providing sufficient I/O bandwidth to exchange data with other modules in the AdvancedTCA system.

Mercury's FPGA co-processing and Xeon server-class AdvancedTCA ecosystem is the most powerful Open System Architecture (OSA) available for general processing, enabling true on-platform cloud processing at the tactical edge. Eminently suited to radar, electro-optical/infrared (EO/IR), complex image intelligence (IMINT), sensor fusion and artificial intelligence (AI) applications, this standard 8U AdvancedTCA form-factor blades enables compute capabilities that were never previously possible, adding new functionality and autonomy to modern missions.

FPGA-Based Pre-Processing

EnsembleSeries FCN8604 blades feature two Intel Arria 10 FPGAs, each with a QFSP+ interface to the front-panel of the module, and an additional I/O interfaces to the zone 3 user I/O region. This allows raw sensor data to stream directly into the FPGA, allowing for optimized preprocessing and co-processing algorithms to operate on the data before handing it off to the Intel general-purpose processing elements. Each FPGA features 32 GB of DDR4-2133 memory in 4 banks to support co-processing operations, and is linked to the Intel Xeon processor complex by dual x8 Gen3 PCle interfaces.













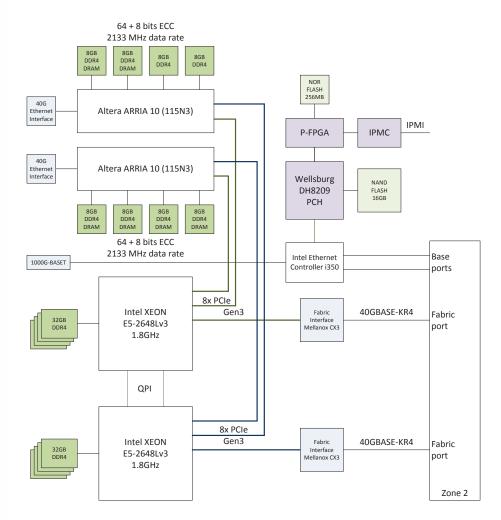


Figure 1: FCN8604 functional block diagram

Xeon Server-Class Ecosystem

EnsembleSeries FCN8604 blades feature two Intel 64-bit Xeon (E5-2648L v3) 12-core processors. FCN8604 blades utilizes robust packaging technologies to support two instances of high-pin-count land grid array (LGA) processors in a rugged, embedded form-factor. The dual 12-core processors are linked via two instances of the high-speed, low-latency quick path interconnect (QPI) interface, each of which provides a 38 GB/s (bi-directional) data transfer rate, for a total of 76 GB/s of bandwidth between processors. This interconnected processor architecture is optimized for the intense data movement needed by high performance processing algorithms, such as all-to-all corner turn operations. From a software perspective, this QPI architecture allows EnsembleSeries FCN8604 blades to be configured with a single kernel NUMA-aware operating system running across both processor devices.

Each processor is capable of delivering a 0.69 TFLOPS (peak), with four high-speed, 17.1 GB/s memory bandwidth for each of its four DDR4-2133 memory channels, for a total theoretical peak of 1.38 TFLOPS and 136 GB/s total raw memory bandwidth. The EnsembleSeries FCN8604 refines the innovative standing memory technology first seen on the 6U EnsembleSeries OpenVPXTM HDS6600, HDS6601

and HDS6602 to support up to 128 GB of DRAM on-board in a highly rugged configuration. Native Gen3 PCIe support is also featured on this processor, linking the processing resources directly to the I/O sources on the module. FCN8604 blades also make use of the Wellsburg platform controller hub (PCH) chipset, which provides additional I/O bridging between the Intel processor and external devices.

Each Intel E5-2648L v3 processor includes a large 30 MB cache, shared between the cores, allowing many high performance calculations to remain cache resident; this accelerates processing by eliminating the potential latency required to access DRAM to fetch upcoming data. The Haswell Bridge family of processors supports the proven AVX2 instruction set, delivering an increase in floating-point algorithm performance that is portable to future Intel architectures. Each core provides the AVX2-capable fused, multiple-add (FMA) vector engine, which is especially valuable for radar processing applications which routinely requires the summing of vast arrays of multiplied numbers. The inclusion of FMA support enables complex radar and similar operations to be performed approximately twice as quickly as before. Fast Fourier transformations (FFTs) and other matrix manipulations are handled more efficiently with Intel's server-class memory and processing optimizations in the Xeon E5 processor family.

High Speed Fabric Interfaces

EnsembleSeries FCN8604 blades feature Mellanox's ConnectX-3 host adaptors for fabric channel communications. Bridging between the native Gen3 PCle interfaces on the Intel processors and the AdvancedTCA fabric channel, the ConnectX-3 can be configured to support InfiniBand (DDR, QDR or FDR10) or 10/40 Gb/s Ethernet as the data protocol. This approach scales the fabric channel bandwidth to a peak theoretical rate of up to 5 GB/s per port, or 10 GB/s aggregate across the entire dual-port fabric channel. By scaling the fabric channel bandwidth to match the increase in processing performance, the EnsembleSeries FCN8604 architecture ensures that the processor is never starved for data.

System Management

EnsembleSeries FCN8604 blades implement the advanced system management functionality inherent to the AdvancedTCA specification to enable remote monitoring, alarm management, hardware revision and health status. Using the standard IPMI-A and IPMI-B bus, intelligent platform management controller (IPMC), and IPMI protocol, the on-board system management block implementation is designed to comply with PICMG 3.0. This allows the EnsembleSeries FCN8604 blades to:

- Read sensor values
- Read and write sensor thresholds, allowing an application to react to thermal, voltage, or current variations that exceed those thresholds
- · Reset the entire blade
- Power up/down the entire blade
- Retrieve blade field replaceable unit (FRU) information

Additional Features

In addition to the sophisticated management subsystem and fabric interconnect, the EnsembleSeries FCN8604 blade provides users with a toolkit enabling many different application features including:

- Thermal and voltage sensors integrated on-board
- Real-time clock with accuracy better than 10ppm <1second per day
- General purpose timers
- Watchdog timer to support interrupt or reset
- Multiple boot paths, include netboot, USB boot, boot from SATA, or from the on-board flash device

Open Software Environment

Mercury leverages over 35 years of multi-computing software expertise, including multicore processor expertise, across its many platforms. This strategy is fully applied to the EnsembleSeries FCN8604 blade. The same Linux® development and run-time environment is implemented on the EnsembleSeries FCN8604 blade as on other Intel based Mercury modules across the EnsembleSeries 3000 and 6000 Series. Off-the-shelf open software such as OFED and OpenMPI are fully supported by the Mellanox ConnectX-3 fabric channel.

Mercury Sensor Processing Ecosystem

Modern sensor compute subassemblies are customized assemblies of interoperable building blocks built to open standards. Mercury's hardware and software portfolio of building blocks are physically and electrically interoperable as defined by international industrial standards, including VITA's OpenVPX standards. Mercury subsystems are designed from a suite of sophisticated open architecture building blocks that are combined and scaled to meet a broad range of advanced sensor chain processing requirements.

Mercury subsystems may include analog, digital and mixed-signal receiver modules, single-board computers and signal processing payload modules. Payloads may have acquisition, digitization, processing, and exploitation and dissemination elements and include FPGA, CPU, GPU or ADC/DAC technology, and be made up of multiple subsystems developed to multiple standards, including OpenVPX and others such as ATCA, ATX/E-ATX, or VME/VXS.

Specifications

Processors

Dual Intel 1.8GHz 12-core E5-2648L v3 (Wellsburg Bridge) Xeon server-class processors

Haswell architecture with Fused Multiple-Add and AVX2

Peak performance 1.38 TFLOPS per module

Threads per core 2

QPI interface between processors

38 GB/s bi-directional (76 GB/s between processors)

Dual Integrated x16 Gen3 PCle interface

Dual FPGA pre-processors

Intel Arria 10

Memory

Xeon processors

32GB or 64GB DDR4-2133 with ECC per CPU (128GB total max.)

FPGA pre-processors

32GB DDR4 - 2133 per FPGA (64GB total)

Flash

SPI flash: Dual 16 MB partitions NAND flash: 8 GB, SATA interface

Fabric Channel

Mellanox ConnectX-3 VPI host adapter
Support DDR/QDR/FDR10 InfiniBand or 40/10 Gb/s Ethernet protocols

Mechanical

8U AdvancedTCA PICMG 3.0

1.2" slot pitch

Power consumption:

- ~75W per processor
- ~260W per module

Environmental

Standard ATCA Product Environmental Qualification Levels

		Qualification Levels	
Cooling Method		Air-cooled	
Ruggedness		ATCA Rugged Level A1	ATCA Rugged Level A2
Temperature	Operating	0°C to +40°C	-5°C to +40°C
	Storage	-40°C to +70°C	-40°C to +85°C
Humidity	Aggravated	10-90%, non-condensing	
Altitude	Operating	0-10,000ft	
	Storage	0-30,000ft	
Vibration	Sine	0.5G Sine, 5-100 Hz, .25 Octave/min, 1 hr/axis	1G Sine, 5-100 Hz, .25 Octave/min, 1 hr/axis
	Shock	z-axis: 10g; x and y-axes: 10g; (11ms 1/2-sine pulse, 3 positive, 3 negative)	z-axis: 10g; x and y-axes: 30g; (11ms 1/2-sine pulse, 3 positive, 3 negative)
Required Flow Rate		Contact Factory	

Additional Services			
Optional Environmental Screening and Analysis Services			
Cold Start Testing Cold Soak Testing Custom Vibration CFD Thermal Analysis Finite Element Analysis	Safety Margin Analysis Temperature Cycling Power Cycling Environmental Stress Screening		
Standard Module, Optional Services			
Engineering Change Order (ECO) Notification ECO Control Custom Certificate of Conformity (CofC) Custom UID Labeling	Alternate Mean Time Between Failure (MTBF) Calculations Hazmat Analysis Diminished Manufacturing Sources (DMS) Management Longevity of Supply (LOS) Longevity of Repair (LOR)		
Contact factory for additional information			

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