# mercury systems™

# Ensemble**Series**™ DCM3110

Configurable, low-latency, coherent 3U OpenVPX Rx/Tx module



- Coherent and spectrally pure for EW applications
- Kintex® UltraScale™ FPGA processing power
- Low-latency ADC and DAC performance
- Multi-channel/Multi-card synchronization
- Built-in IPMI controller



Mercury's EnsembleSeries® DCM3110 is a versatile Open-VPX Rx/Tx module packaged in an efficient 3U form-factor. These modules are designed for use in ultra-low latency transceivers/DRFMs, GSPS receivers and GSPS AWGs (Arbitrary Waveform Generator) applications within the EW (EP and EA), ultra-wideband comms and SAR domains. Each module delivers excellent spectral purity and efficiency, coherent multi-channel/board functionality and excellent LPOI (Low Probability Of Intercept) signal detection capability. The EnsembleSeries DCM3110 has the highest spectral efficiency available, blending performance, quick reaction time and processing power within the small 3U form-factor.

Performance of the EnsembleSeries DCM3110 is derived from its 12-bit TI (ADC12D2000RF) powered ADC frontend and the processing power of the Kintex UltraScale KU115 FPGA. Both elements represent the most contemporary technology available. The module's conversion characteristics are illustrated in Fig 1 (ADC/DAC performance plots). This level of performance is achieved by ideally pairing the backend digital/analog conversion to the frontend analog/digital conversion, achieving an optimized balance of detection, speed and latency performance. This performance profile is especially suited to EW applications.

Within the domain of EW, it is often difficult to discern small signals within a cluttered environment, continually over a wide bandwidth (e.g. 1 GHz IBW). Discerning the signal of interest and then transmitting an accurate response to the threat compounds the challenge. Best solutions to this type of application require exceptional frontend precision, over a wide, instantons bandwidth and the fastest responses. The Ensemble-Series DCM3110 is engineered to meet these needs which are illustrated by the plot shown in Fig 2 (performance Vs reaction time).

EnsembleSeries DCM3110 transceivers have configurable ADCs which may be arranged as four units, each with up to 2.0 GSPS (Giga Samples Per Second) in non-DES mode (Dual Edge Sampling). Alternatively, the ADCs may be configured as two interleaved units with up to 3.0 GSPS/ea. (4 GSPS uncharacterized), both operating in DES mode. Each EnsembleSeries DCM3110 has two, low-latency DACs, each running up to 3.0 GSPS with selectable output modes (return to zero, non-return to zero, narrow return to zero, RF).

The EnsembleSeries DCM3110 is driven by a powerful Kintex UltraScale FPGA that is supported by 4GB of external DDR4 memory. The KU115 maximizes bandwidth on/off card by way of two x8 full-duplex MGT links on P1 (one defaults to PCle Gen3 x8) and 32 LVDS pairs on P2 for synchronous, low-latency communications. DCM3110s are equipped with an IPMI (Intelligent Platform Management Interface) controller and are available in reliable, compact, rugged air- or conduction cooled 3U OpenVPX packages.

Mercury Systems is the better alternative for affordable, secure processing subsystems designed and made in the USA. These capabilities make us the first commercially based defense electronics company built to meet rapidly evolving next-generation defense challenges.



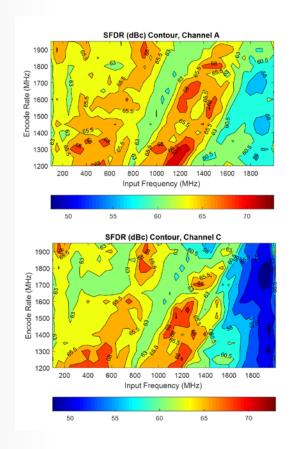


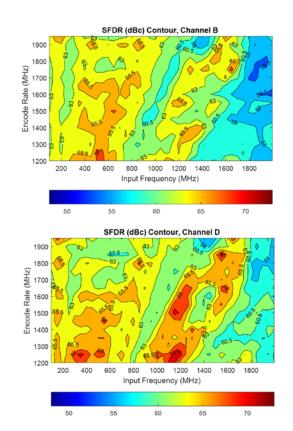




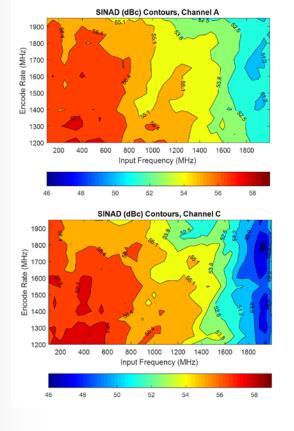


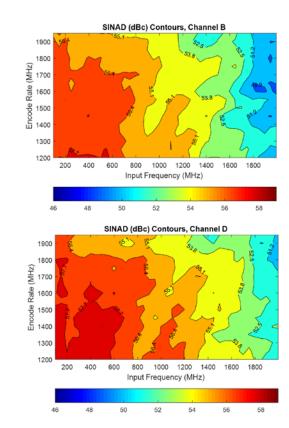


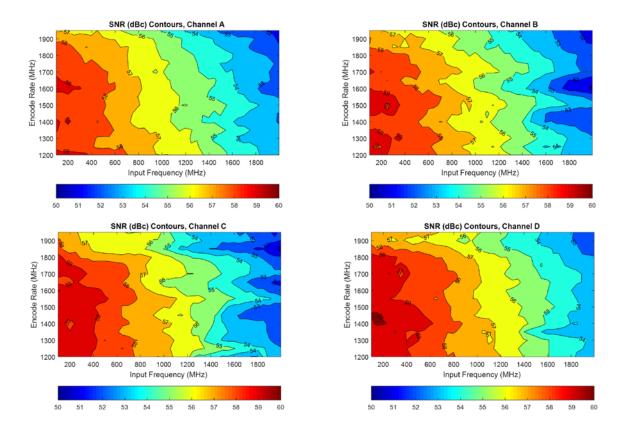




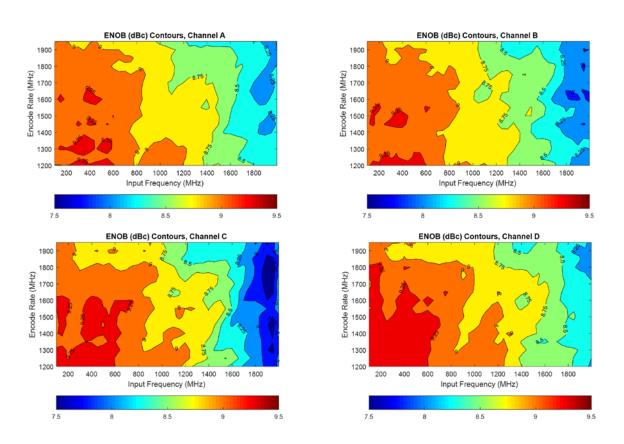
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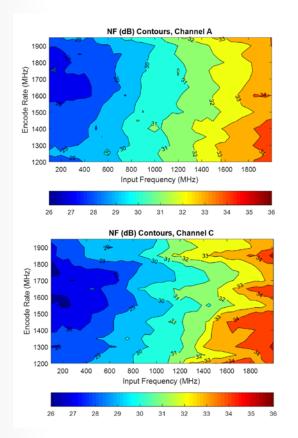


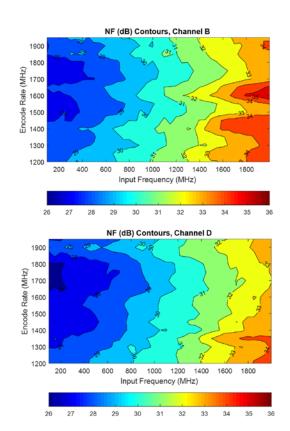




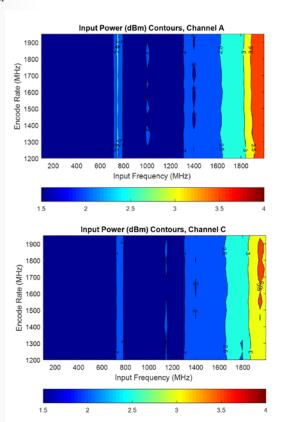
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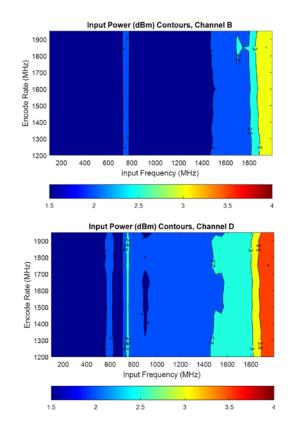


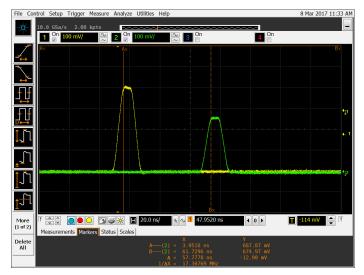




# **INPUT POWER**







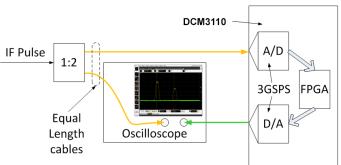


Figure 2. Performance Vs reaction time and equipment setup

# EchoCore™ Advantage

EchoCore firmware streamlines the development of FPGA-based applications. These cores are optimized to maximum performance for a range of common operations. They are designed with straightforward interfaces for application-specific algorithm IP, greatly reducing the time required to create complete solutions.

# Mercury's OpenVPX Ecosystem

Sensor processing chain awareness, building blocks and ecosystem sensor chain awareness is having the technical expertise and resources to design and build capable, compatible solutions along the whole sensor processor chain. From RF/microwave, digital/analog signal manipulation to dense, SWaP optimized processing resources to actionable intelligence dissemination; Mercury's rugged processing subassemblies leverage the best commercial-item technology, enabling prime contractors to win more business. Modern sensor processing subassemblies are customized assemblies of interoperable building blocks built to open standard architectures. Mercury's hardware and software portfolio of building blocks are physically and electrically interoperable as defined by international industrial standards, including OpenVPX.

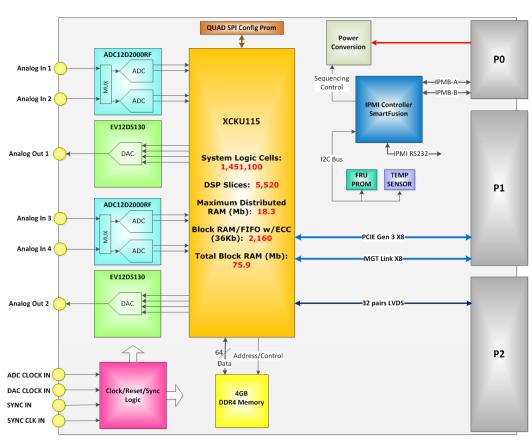


Figure 3. EnsembleSeries DCM3110 functional block diagram

# **Specifications**

# **OpenVPX** packaging

OpenVPX System Standard (VITA 65) encompasses: VITA 46.0, 46.3, 46.4, 46.6, 46.11, and VITA 48.1, 48.2 (REDI)

#### **Processor**

Kintex Ultrascale KU115 FPGA 5,520 DSP slices PCle Gen 3 x8 to P1 MGT x8 to P1

#### **Memory**

4GB DDR4 (512M x 64)

# **DACs**

2 DACs up to 3.0 GSPS

Selectable output modes (Return to zero, non return to zero, narrow return to zero and RF)

# **Optional ADC configurations**

4 single ADCs up to 2.0 GSPS/ea. (in non-dual edge sampling mode)

2 interleaved ADCs each up to 3.0 GSPS/ea. (in dual edge sampling mode)

#### **Other**

Integrated IPMI controller

Environmental		Environmental Qualification Levels		
		Air-cooled		Conduction-cooled
		Commercial L0	Rugged L1	Rugged L3
Ruggedness		•	••	•••
Moisture/dust protection		•	••	•••
Temperature	Operating*	0°C to +40°C	-25°C to +55°C	-40°C to +71°C
Operating temperature maximum rate of change		N/A	5°C/min	10°C/min
Temperature	Storage	-40°C to +85°C	-55°C to +85°C	-55°C to +125°C
Humidity	Operating*	10-90%, non-condensing	5-95%, non-condensing	5-95%, non-condensing, 100% condensing
	Storage	10-90%, non-condensing	5-95%, non-condensing	5-95%, non-condensing, 100% condensing
Altitude	Operating*	0-10,000ft	0-30,000ft	0-70,000ft
	Storage	0-30,000ft	0-50,000ft	0-70,000ft
Vibration	Random	0.003 g2/Hz; 20-2000 Hz, 1 hr/axis	0.04 g2/Hz; 20-2000 Hz, 1 hr/axis	0.1 g2/Hz;, 5-2000 Hz, 1 hr/axis
	Sine	N/A	N/A	10G peak;, 5-2000 Hz, 1 hr/axis
	Shock	z-axis: 20g; x and y-axes: 32g; (11ms 1/2-sine pulse, 3 positive, 3 negative)	z-axis: 50g; x and y-axes: 80g; (11ms 1/2-sine pulse, 3 positive, 3 negative)	z-axis: 50g; x and y-axes: 80g; (11ms 1/2-sine pulse, 3 positive, 3 negative)
Salt/Fog		N/A	Contact Factory	10% NaCl
VITA 47		Contact Factory		

<sup>\*</sup> Customer must maintain required cfm level. Consult factory for the required flow rates.

Storage Temperature is defined per MIL-STD-810F, Method 502.4, para 4.5.2, where the product under non-operational test is brought to an initial high temperature cycle to remove moisture. Then the unit under non-operational test will be brought to the low storage temperature. The low temperature test is maintained for 2 hours. The product is then brought to the high storage temperature and is maintained for 2 hours. The product is then brought back to ambient temperature transitions are at a maximum rate of 10°C/min. One cold/hot cycle constitutes the complete non-operational storage temperature test. This assumes that the board level products are individually packaged in accordance with ASTM-D-3951 approved storage containers. These tests are not performed in Mercury shipping containers, but in an unrestrained condition. Please consult the factory if you would like additional test details.

All products manufactured by Mercury meet elements of the following specifications: MIL-STD-454, MIL-STD-883, MIL-HDBK-217F, and MIL-I-46058 or IPC-CC-830, and various IPC standards. Mercury's inspection system has been certified in accordance with MIL-I-45208A.

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INNOVATION THAT MATTERS ™

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<sup>\*\*</sup> Card edge should be maintained below 71°C